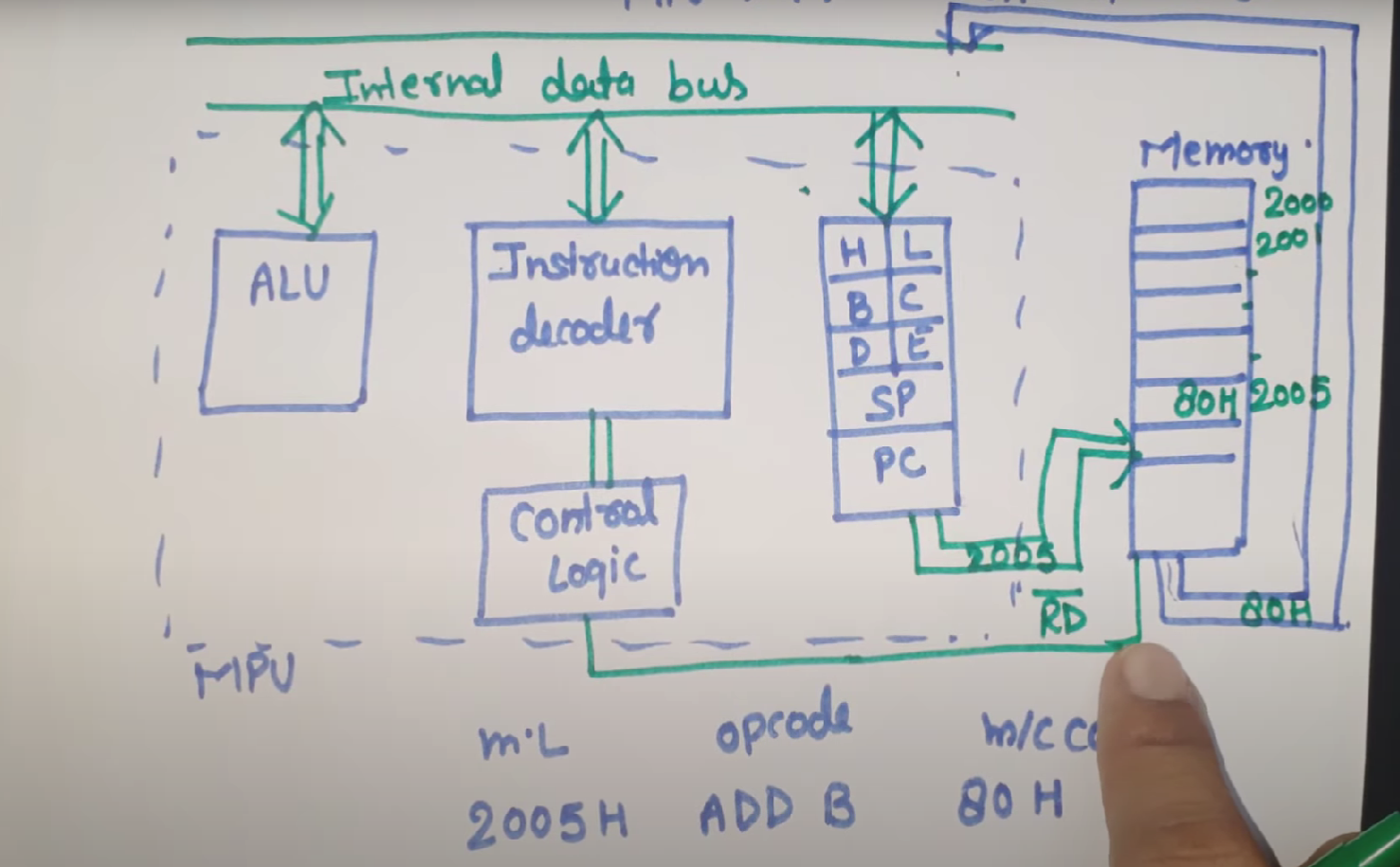
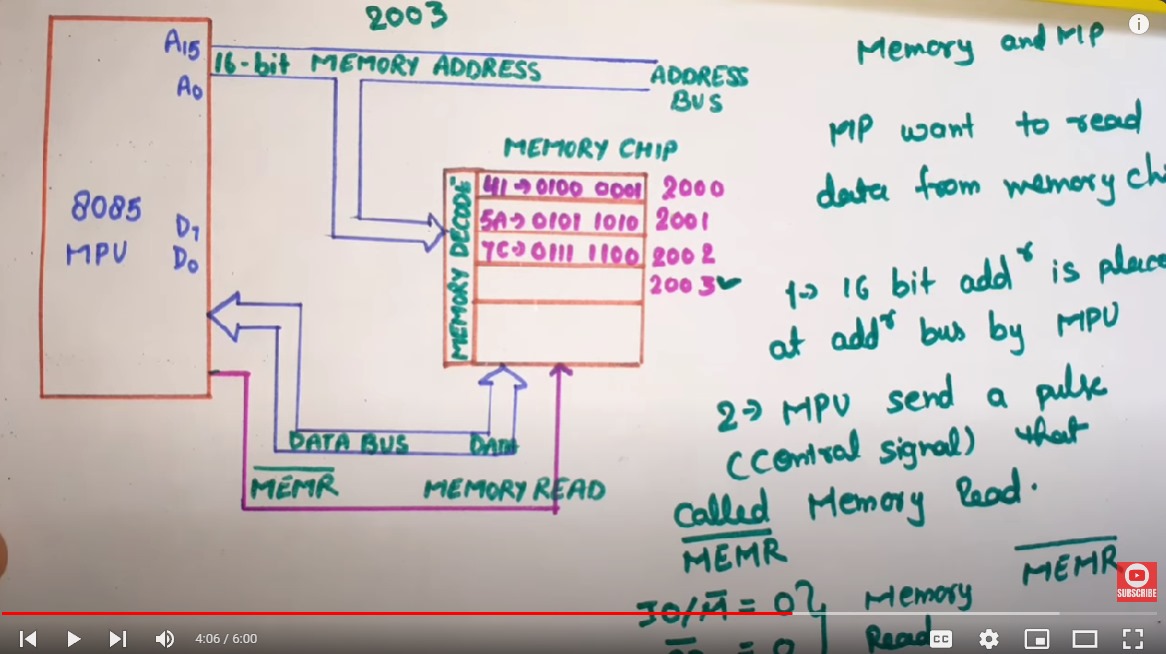
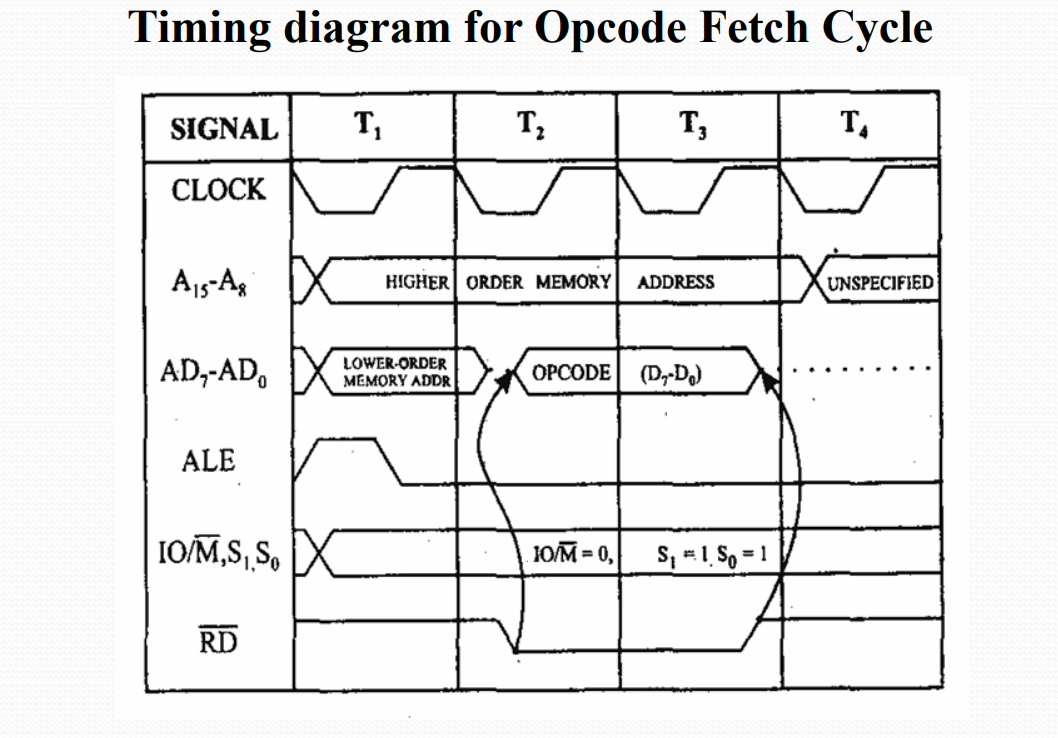
Data Flow in Microprocessor 8085

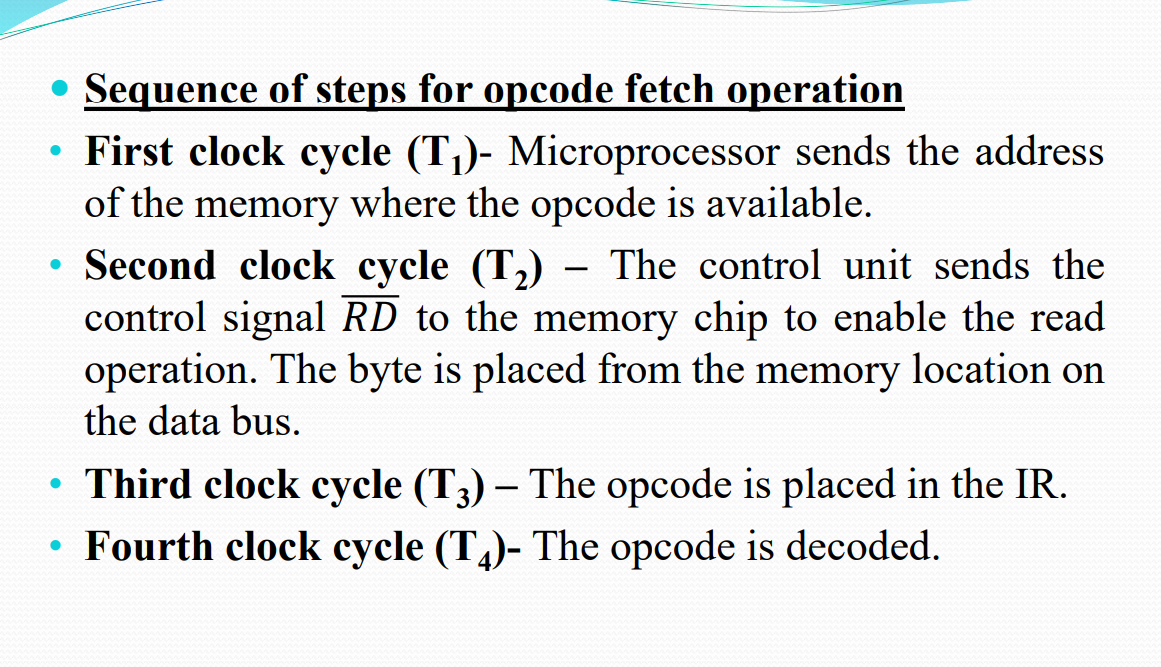


Memory Interfacing With 8085



Timing Diagram for 8085 Op code fetch





Fetch Operation + Memory Read

